

ABSTRACT

A display device includes a plurality of light emitting elements arranged in a matrix. A scan signal is made to flow into a gate signal line and a data signal is made to flow into a source signal line so that the data signal is applied to a source electrode and the scan signal is supplied to a gate electrode of a control TFT arranged at a portion where the both signal lines intersect when viewed from above. Thus, when the control TFT is turned ON, a drive TFT having a gate electrode connected to the drain electrode is turned ON, so that current is supplied from a power supply line via the source electrode and the drain electrode of the drive TFT to an organic EL element and the organic EL element emits light. A holding capacity is present between the control TFT and the drive TFT. Even when the scan signal becomes LOW level and the control TFT turns OFF, the gate potential of the drive TFT is held for a predetermined period of time by the holding capacity and the organic EL element continues to emit light.